

**PATENT NUMBER**

**PATENT DATE**

## Q.A

**EXAMINER**

2634

XAMINER  
~~CHITOUR~~

Naohiro Matsui

Phase capacitor, and phase locked loop circuit having the same as well as method of phase comparison

PTO-2040  
12/89[illegible]☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
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				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	

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